

Patent Abstracts of Japan

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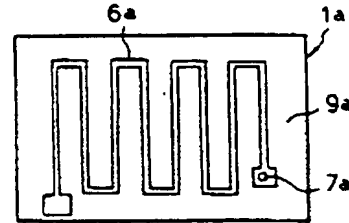
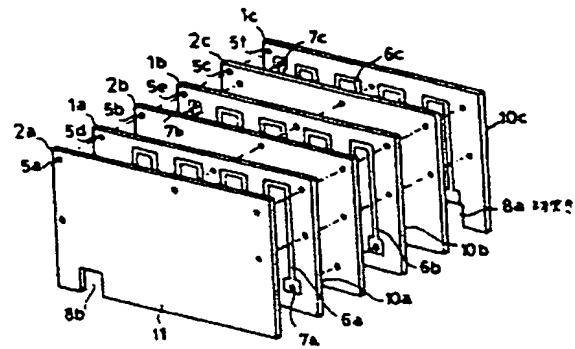
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APPLICATION NUMBER : 62299725

APPLICANT : NEC CORP;

INVENTOR : IZUMI HIROAKI;

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TITLE : DELAY LINE



ABSTRACT : PURPOSE: To attain the length of a delay line with high accuracy by overlapping alternately plural MIC boards with a ground face and a signal line formed thereupon and insulating boards without any conductor on the front and rear sides, connecting the ground face and the signal line respectively electrically and constituting the desired delay line having the sum of length of the signal lines to be connected.

CONSTITUTION: A strip line 6a is formed on one side of an insulation board 9a of MIC boards 1a~1c to constitute a delay line. A ground face 10a made of conductors on the entire face is formed to the rear side of the insulation board 9a. Then strip lines 6a~6c of the MIC boards 1a~1c are connected through through holes 7a~7c opened to the insulation boards 2a~2c and ground faces 10a~10c are connected through through holes 5a~5c provided on the insulation boards 2a~2c and through holes 5d~5f provided on the MIC boards 1a~1c. Thus, the small-sized delay line with a small space is constituted and the delay time is adjusted with high accuracy.

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